

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	Hong Jiang et al.	§	Art Unit:	2195
		§		
		§	Examiner:	Eric Charles Wai
Serial No.:	10/750,583	§		
		§		
Filed:	December 31, 2003	§	Conf. No.:	8582
		§		
		§	Docket:	ITL.1704US
For:	Processing Architecture Having	§		P17510
	Passive Threads and Active	§		
	Semaphores	§	Assignee:	Intel Corporation

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO FINAL REJECTION**

Sir:

In response to the final rejection mailed May 10, 2010, please amend the above-referenced patent application as follows:

Date of Deposit: June 18, 2010  
I hereby certify that this correspondence is being electronically  
transmitted on the date indicated above.  
*Cynthia L. Hayden*  
Cynthia L. Hayden